



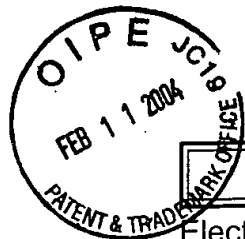
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Electronic Patent Application Submission
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EFS ID: 55326
Application ID: 10684713
Title of Invention: Multiple Operating Voltage
Vertical Replacement-Gate (VRG)
Transistor
First Named Inventor: Paul Layman
Domestic/Foreign Application: Domestic Application
Filing Date: 2003-10-14
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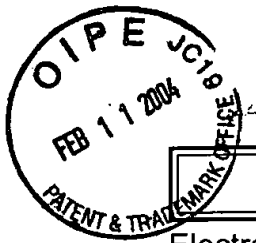
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Title of Invention	Multiple Operating Voltage Vertical Replacement-Gate (VRG) Transistor									
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>John L. DeAngelis Jr. Registered Number: 30,622</td><td>/John L. DeAngelis, Jr./</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	John L. DeAngelis Jr. Registered Number: 30,622	/John L. DeAngelis, Jr./	Attorney		
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<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>Agere282-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	Agere282-usidst.xml		us-ids.dtd		us-ids.xsl
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Comments										



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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	14	4837606	1989-06-06	Goodman, et al
	15	5342797	1994-08-30	Sapp, et al
	16	6027975	2000-02-22	Hergenrother, et al
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	23	5668391	1997-09-16	Kim, et al
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	27	5208172	1993-05-04	Fitch, et al
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	30	5864158	1999-01-26	Liu, et al
	31	6268621	2001-07-31	Emmi, et al

Signature

Examiner Name	Date